



RFL Electronics Inc.

INSTRUCTION DATA

RFL 9300 DCE Emulator 105880 105880-1

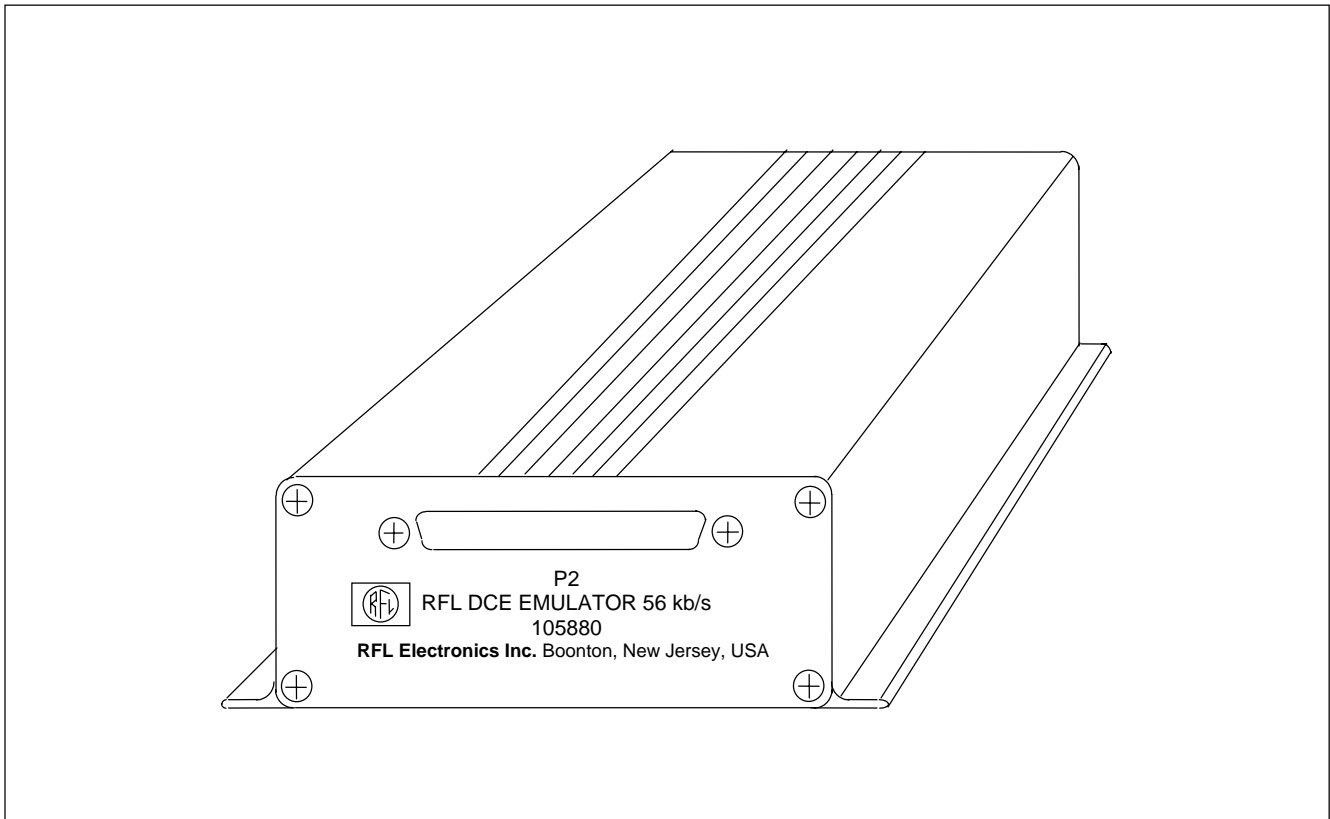


Figure 1. Typical RFL DCE Emulator, front panel view

DESCRIPTION

The RFL DCE Emulator shown in Figure 1, simulates a network that provides a clock source for the RFL 9300. It is normally used in back-to-back relay testing, or in single relay testing using a loopback connector. For back-to-back testing refer to paragraph 7.3 of the RFL 9300 Instruction Manual. For single relay testing refer to paragraph 8.2 of the RFL 9300 Instruction Manual. Refer to Figures 2 through 5 and Tables 1 and 2 of this document for more information.

Table 1. RFL DCE Emulator Characteristics

| Assembly Number | Data Rate | Interface Type | Interface Cable Connector | Pinouts |
|------------------------|------------------|-----------------------|----------------------------------|----------------|
| 105880 | 56 kb/s | RS-449 | 37-pin male | See Table 1 |
| 105880-1 | 64 kb/s | RS-449 | 37-pin male | See Table 1 |

SPECIFICATIONS

As of the date this Instruction Data Sheet was published, the following specifications apply to the RFL DCE Emulator. Because all RFL products undergo constant refinement and improvement, these specifications are subject to change without notice.

Signal Connector Type:

- P1: 37-pin female type D subminiature
- P2: 37-pin female type D subminiature

Data Rate: In accordance with Table 1.

Input Power Requirements:

5 Vdc @ <1Watt

Operating Temperature:

-30⁰C to +60⁰C (-22⁰F to +140⁰F)

Relative Humidity: 95 percent @ 40⁰C, non-condensing.

Dimensions:

Width: 5.13 inches (13 cm)
Height: 1.77 inches (4.5 cm)
Depth: 6.75 inches (17.1 cm)

Weight:

1 lb.

Mounting Dimensions: In accordance with Figure 4.

Table 2. Pinout Information

| Signal | P1 connector | P2 connector |
|--------|--------------|--------------|
| Ground | 1 | 1 |
| TXD | 4 | 4 |
| TXC | 5 | 5 |
| RXD | 6 | 6 |
| RXC | 8 | 8 |
| Common | 19 | 19 |
| TXD | 22 | 22 |
| TXC | 23 | 23 |
| RXD | 24 | 24 |
| RXC | 26 | 26 |

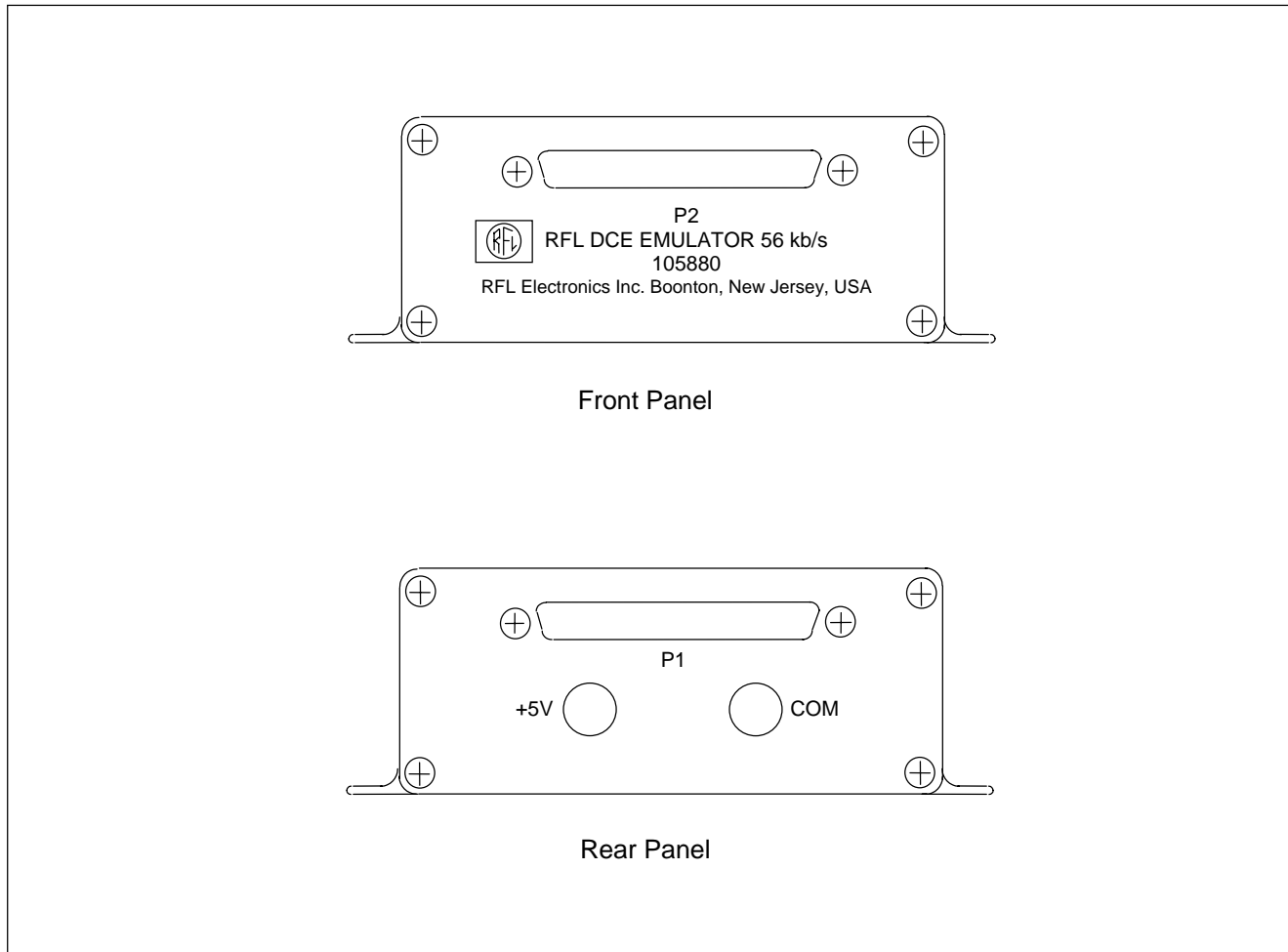
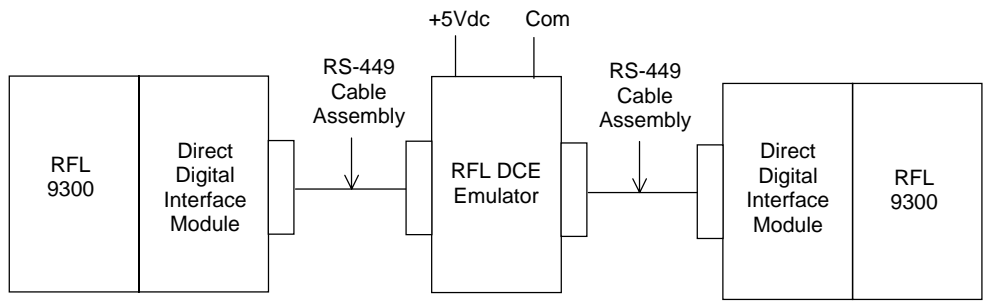
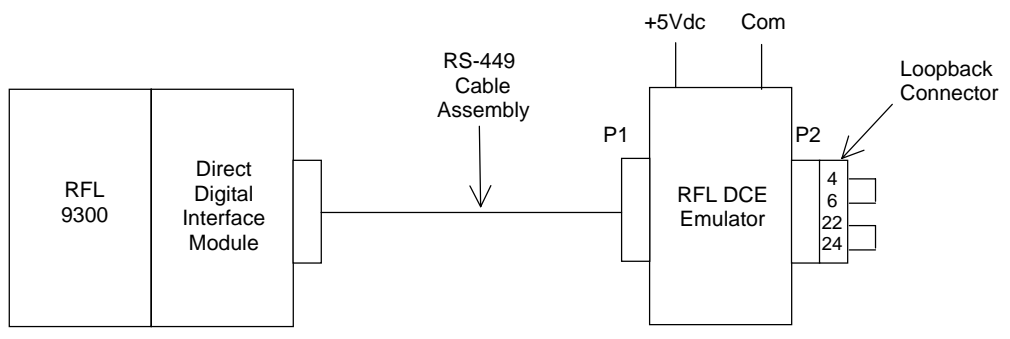


Figure 2. Typical RFL DCE Emulator, front and rear views



a. Back-to-back setup using direct digital interface



b. Single relay test setup using direct digital interface

Figure 3. RFL DCE Emulator, typical applications

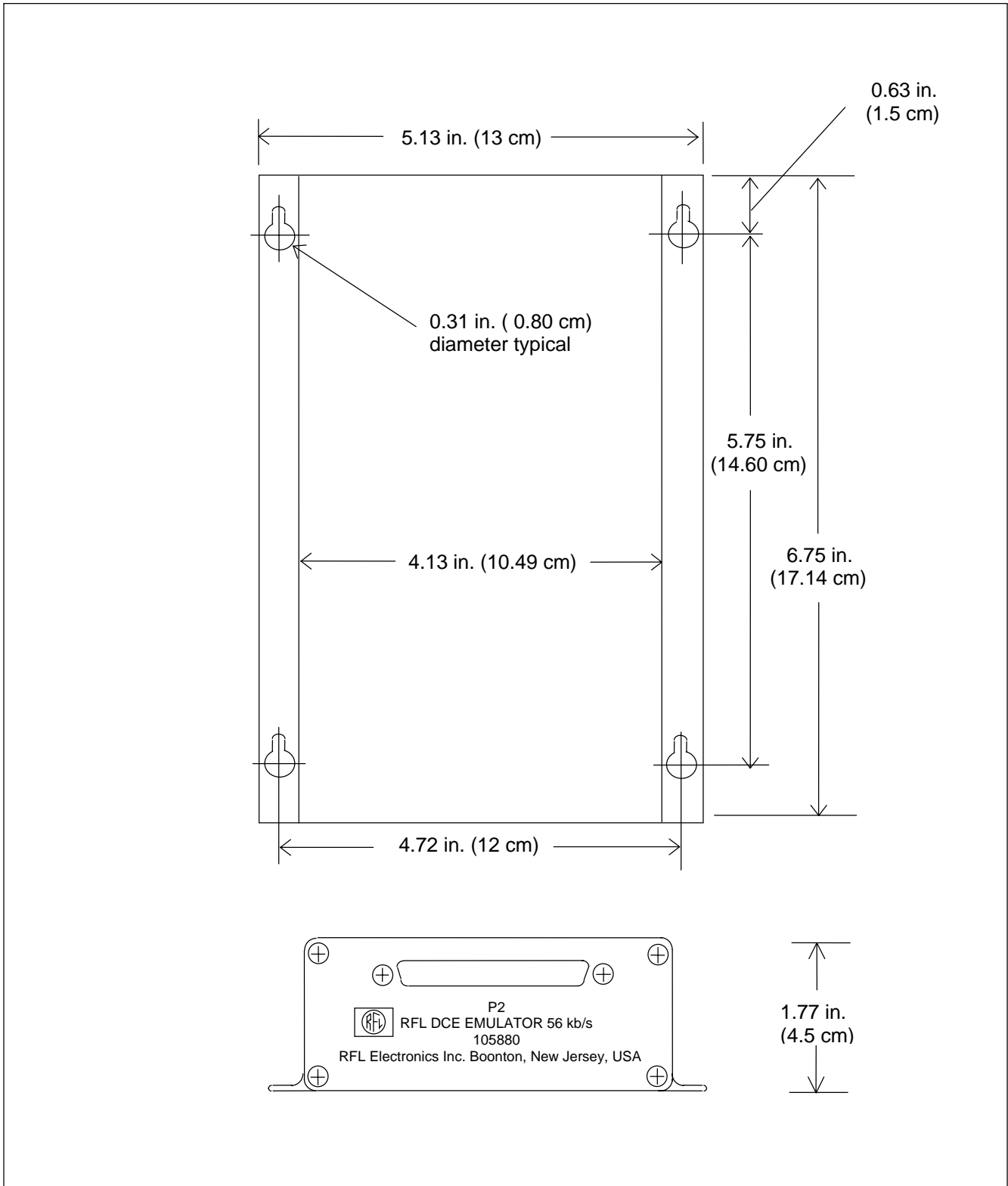


Figure 4. RFL DCE Emulator Mounting Dimensions

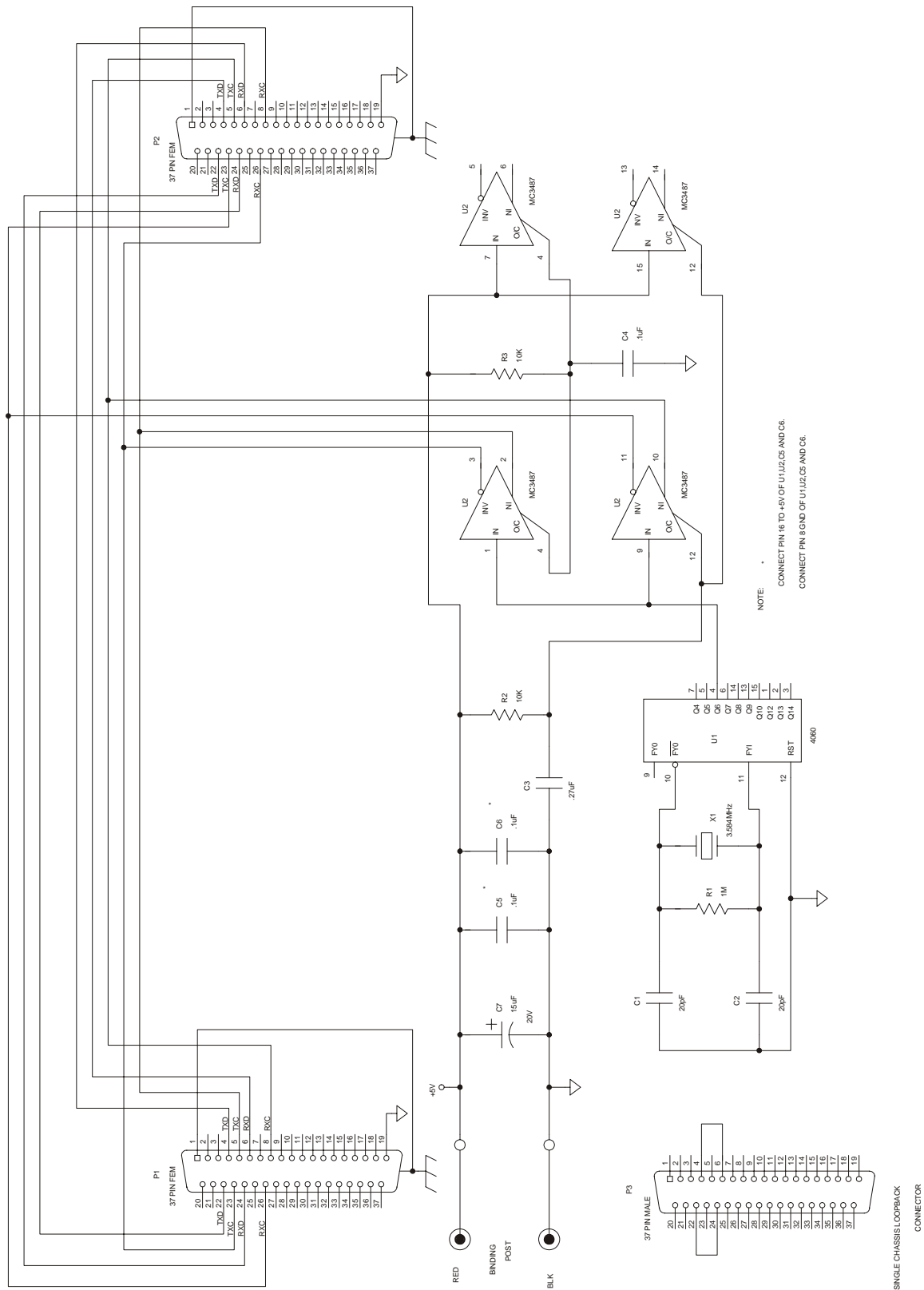


Figure 5. RFL DCE Emulator Schematic (C-105884-B)

NOTES

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